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Docket No.: 21806-00106-US
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Gary S. Ditlow et al.

Application No.: 09/677,913

Confirmation Number: 4668

Filed: October 3, 2000

Art Unit: 2123

For: METHOD OF POWER DISTRIBUTION
ANALYSIS FOR I/O CIRCUITS IN ASIC
DESIGNS

Examiner: S. Broda

SUBMISSION OF DRAWINGS

MS PGPUB Drawings
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Submitted herewith is one set (7 sheets, FIGS. 1-8) of drawings for filing in the above-identified patent application.

Applicant believes no fee is due with this response. However, if a fee is due, please charge our Deposit Account No. 09-0456, under Order No. 21806-00106-US from which the undersigned is authorized to draw.

Dated:

Respectfully submitted,

By 

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